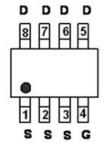
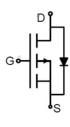


V _{DSS}	-30V			
R _{DS} (on)	19.5m (typ.)			
I _D	-7.8A			







Advanced MOSFET process technology
Special designed for PWM, load switching and
general purpose applications
Ultra low on-resistance with low gate charge
Fast switching and reverse body recovery
150 operating temperature



It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

I _D @ T _C = 25°C	Continuous Drain Current	-7.8		
I _D @ T _C = 100°C	00°C Continuous Drain Current		Α	
I _{DM}	Pulsed Drain Current	-31.2		
P _D @T _A = 25°C	Power Dissipation	2.4	W	
V _{DS}	Drain-Source Voltage	-30	V	
V _{GS}	Gate-to-Source Voltage	± 20	V	
E _{AS}	Single Pulse Avalanche Energy @ L=0.5mH	95	mJ	
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C	



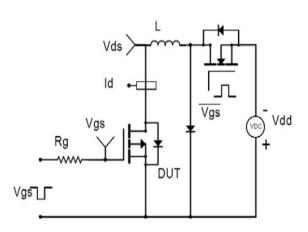
R JA	Junction-to-ambient ()	_	52	°C/W

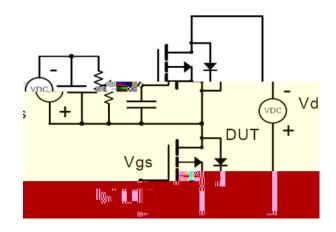
$@T_A=25^{\circ}C$ unless otherwise specified

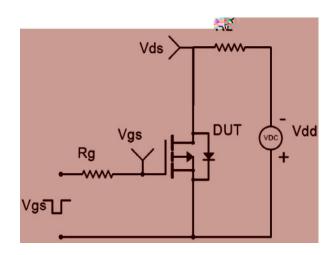
V _{(BR)DSS}	Drain-to-Source breakdown voltage	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
R _{DS(on)}	Static Drain-to-Source on-resistance	_	19.5	25	m	V _{GS} =-10V,I _D = -7.5A
		_	31	41	m	V _{GS} =-4.5V,I _D =-5A
V _{GS(th)}	Gate threshold voltage	-1	_	-2.5	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
I _{DSS}	Drain-to-Source leakage current	_	_	-1	μA	$V_{DS} = -30V, V_{GS} = 0V$
	0.1.10	_	_	100	nA	V _{GS} = 20V
I_{GSS}	Gate-to-Source forward leakage	_	_	-100		V _{GS} = -20V
Qg	Total gate charge	_	22	_		I _D = -20A,
Q _{gs}	Gate-to-Source charge	_	1.5	_	nC	V _{DS} =-15V,
Q _{gd}	Gate-to-Drain("Miller") charge	_	4	_		V _{GS} = -10V
t _{d(on)}	Turn-on delay time	-	12	_		
t _r	Rise time	_	14	_		V _{GS} =-10V, V _{DS} =-15V,
t _{d(off)}	Turn-Off delay time	_	195	_	ns	R _{GEN} =3 ,R _L =0.75
t _f	Fall time	_	95	_		
Ciss	Input capacitance	_	1130	_		V _{GS} = 0V
Coss	Output capacitance	_	185	_	pF	V _{DS} =-15V
C _{rss}	Reverse transfer capacitance	_	115	_		f = 1MHz

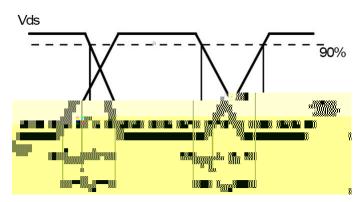
Is	Continuous Source Current	_	_	-7.8	А	MOSFET symbol
	(Body Diode)				<u> </u>	showing the ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐
I _{SM}	Pulsed Source Current	_	_	-31.2	А	integral reverse
	(Body Diode)					p-n junction diode
V _{SD}	Diode Forward Voltage	_	_	-1.2	V	I _S =-20A, V _{GS} =0V
t _{rr}	Reverse Recovery Time	_	36	_	ns	T _J = 25°C, I _F =-10A,
Q _{rr}	Reverse Recovery Charge	_	34	_	nC	di/dt = 100A/µs











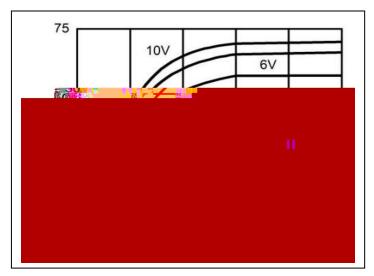
Calculated continuous current based on maximum allowable junction temperature.

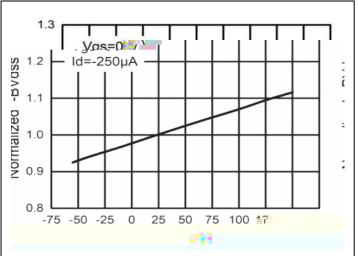
Repetitive rating; pulse width limited by max. junction temperature.

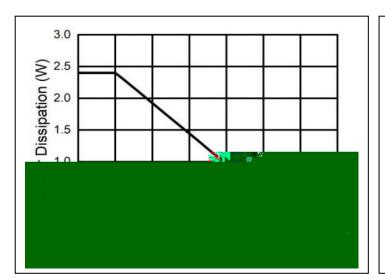
The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.

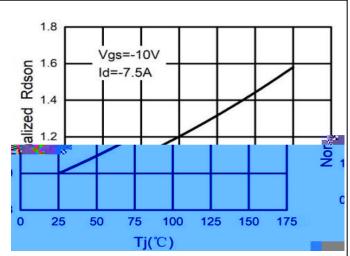
The value of R $_{JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C

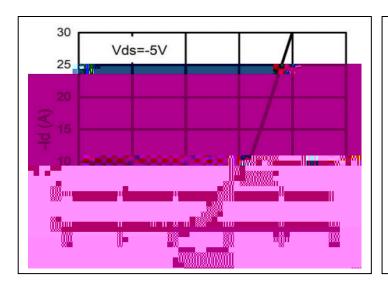


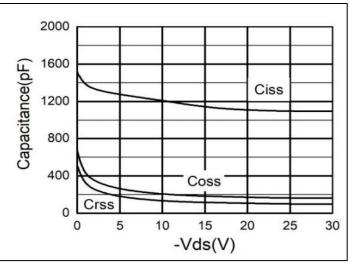




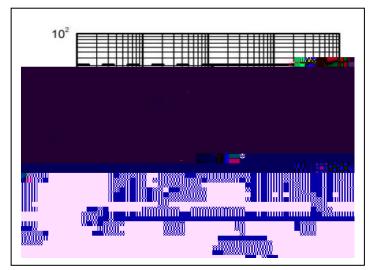


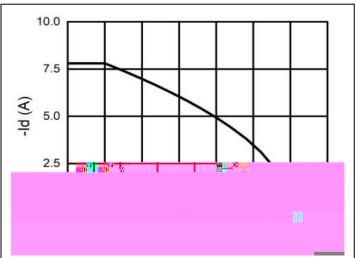


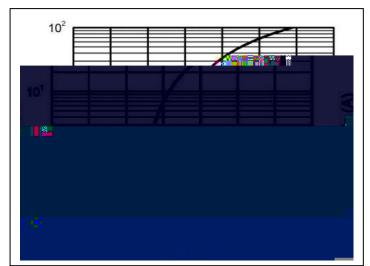


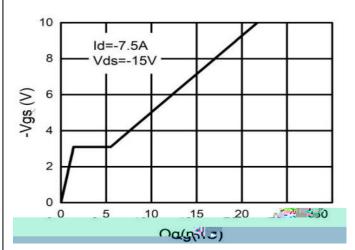
















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