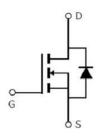


V _{DSS}	30V				
R _{DS} (on)	10m (typ.)				
I _D	20A				





Advanced MOSFET process technology
Special designed for PWM, load switching and
general purpose applications
Ultra low on-resistance with low gate charge
Fast switching and reverse body recovery
150 operating temperature



It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V	20	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V	14	Α
I _{DM}	Pulsed Drain Current	80	
P _D @T _C = 25°C	Power Dissipation	62	W
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-to-Source Voltage	± 20	V
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C

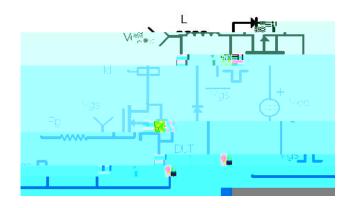


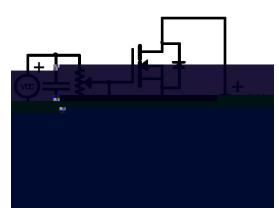
R JC	Junction-to-case	_	2	/W

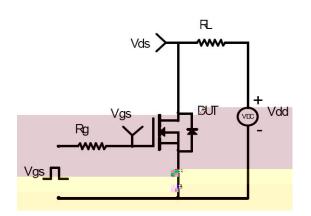
@T_A=25 unless otherwise specified

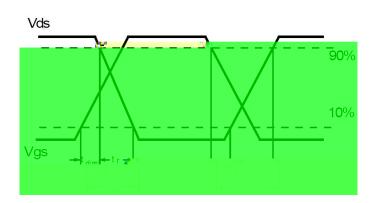
$V_{(BR)DSS} \\$	Drain-to-Source breakdown voltage	30	_	_	V	$V_{GS}=0V,\ I_D=250\mu A$
R _{DS(on)}	Static Drain-to-Source on-resistance	_	10	13	m	$V_{\text{GS}}\text{=}10\text{V}, I_{\text{D}}=15\text{A}$
NDS(on)	Static Dialif-to-Source off-resistance	_	16	22.5	m	V_{GS} =4.5 V , I_D = 10 A
$V_{\text{GS(th)}}$	Gate threshold voltage	1	_	2.2	V	$V_{DS}=V_{GS},\ I_D=250\mu A$
I_{DSS}	Drain-to-Source leakage current	_	_	1	μΑ	$V_{\text{DS}} = 30 \text{V}, V_{\text{GS}} = 0 \text{V}$
I_{GSS}	Gate-to-Source forward leakage	_	_	100	nA	V _{GS} =20V
IGSS	Gate-to-Source forward leakage	_	_	-100	IIA	$V_{GS} = -20V$
C_{iss}	Input dananation	_	805	_		$V_{GS} = 0V$
					pF	№ DS = 15 V
						f =











Calculated continuous current based on maximum allowable junction temperature.

Repetitive rating; pulse width limited by max. junction temperature.

The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.



