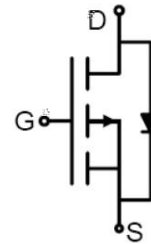
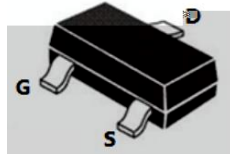


$V_{DSS}$	-12V
$R_{DS(on)}$	13.3m (typ.)
$I_D$	-9A



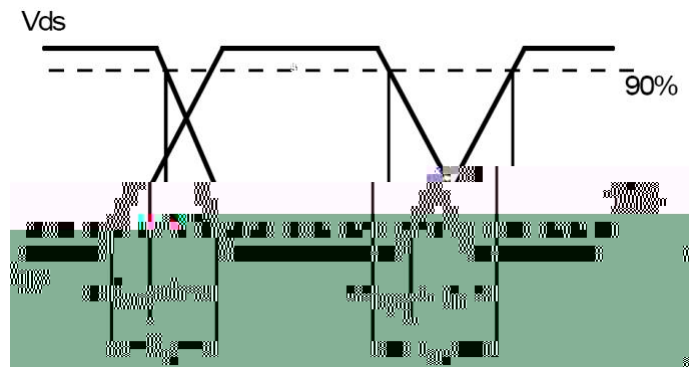
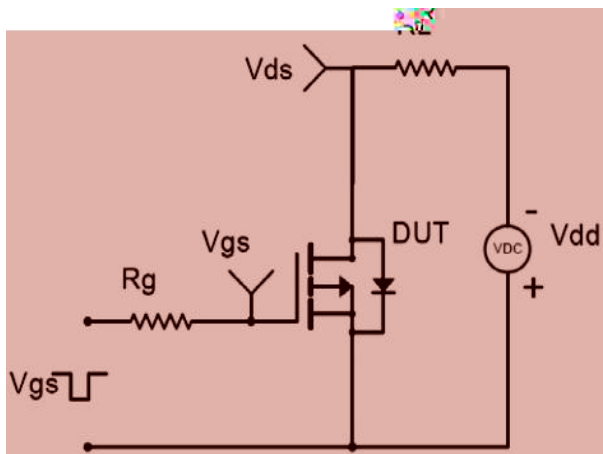
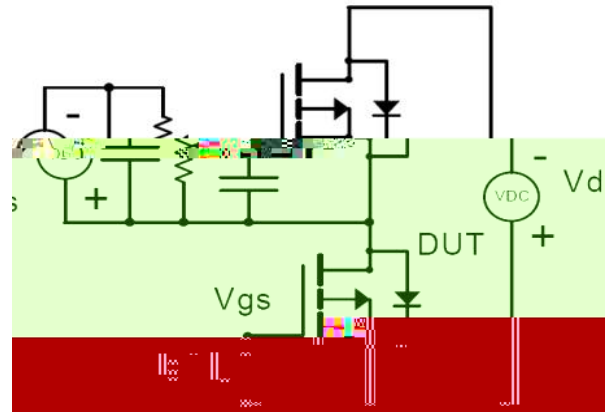
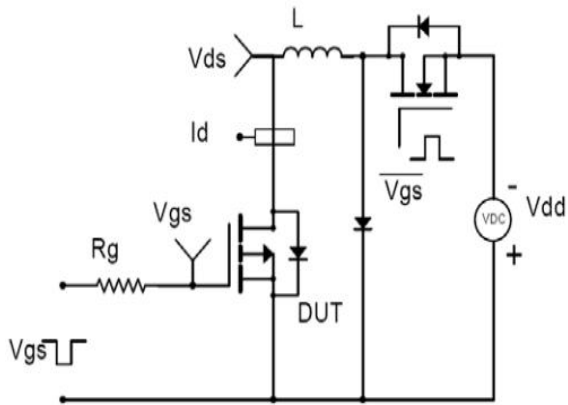
Advanced MOSFET process technology  
 Special designed for PWM, load switching and  
 general purpose applications  
 Ultra low on-resistance with low gate charge  
 Fast switching and reverse body recovery  
 150 operating temperature



It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

$I_D @ T_C = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	-9	A
$I_D @ T_C = 100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	-5.8	
$I_{DM}$	Pulsed Drain Current	-36	
$P_D @ T_C = 25^\circ\text{C}$	Power Dissipation	2	W
$V_{DS}$	Drain-Source Voltage	-12	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 12$	V
$E_{AS}$	Single Pulse Avalanche Energy @ $L=0.5\text{mH}$	20	mJ
$T_J \quad T_{STG}$	Operating Junction and Storage Temperature Range	-55 to +150	$^\circ\text{C}$



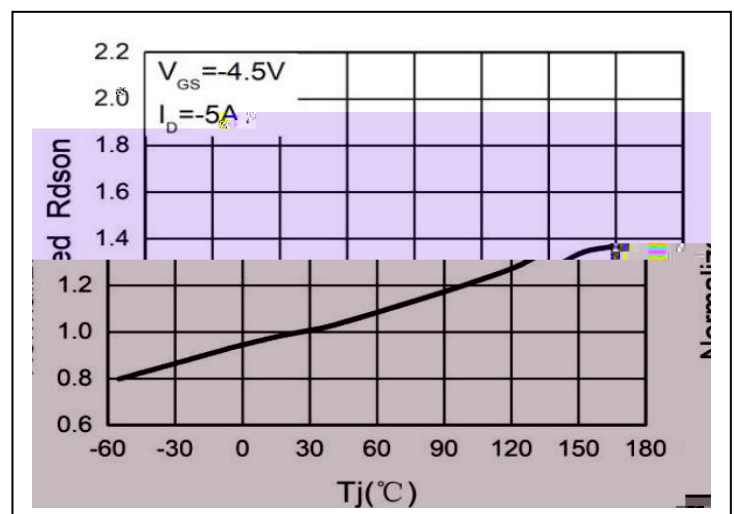
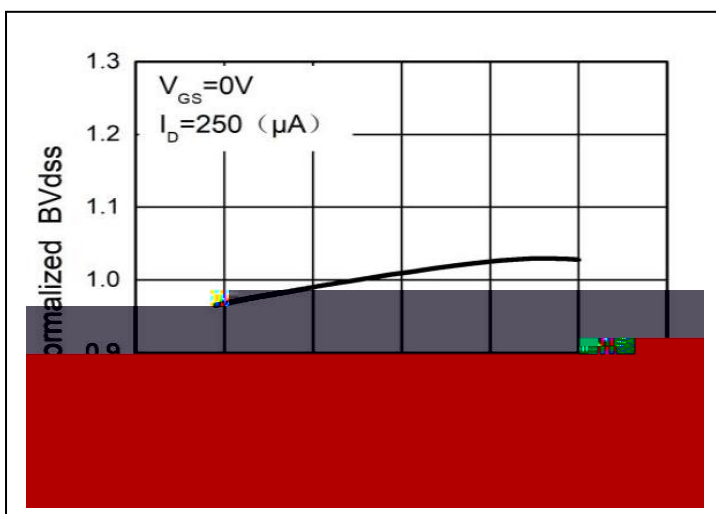
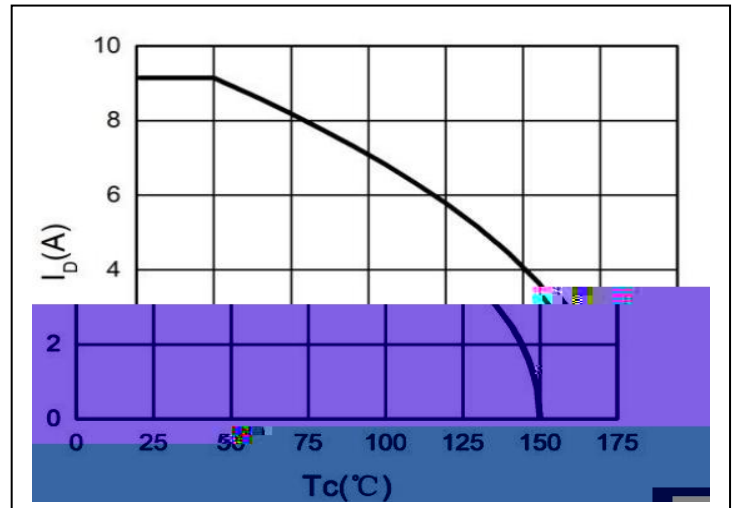
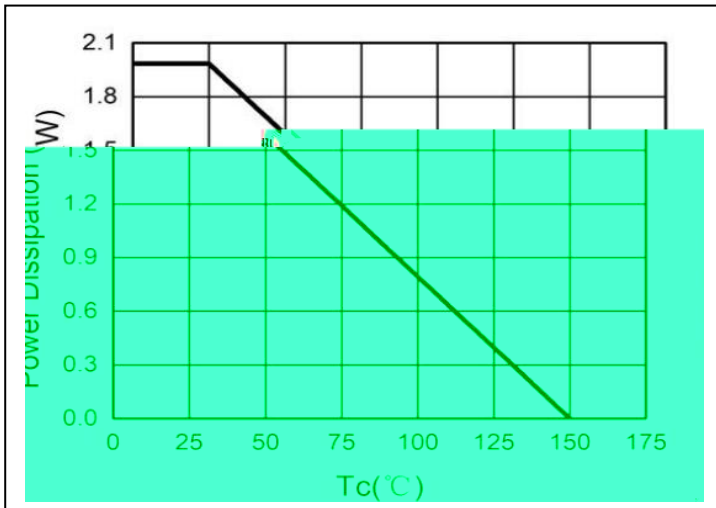
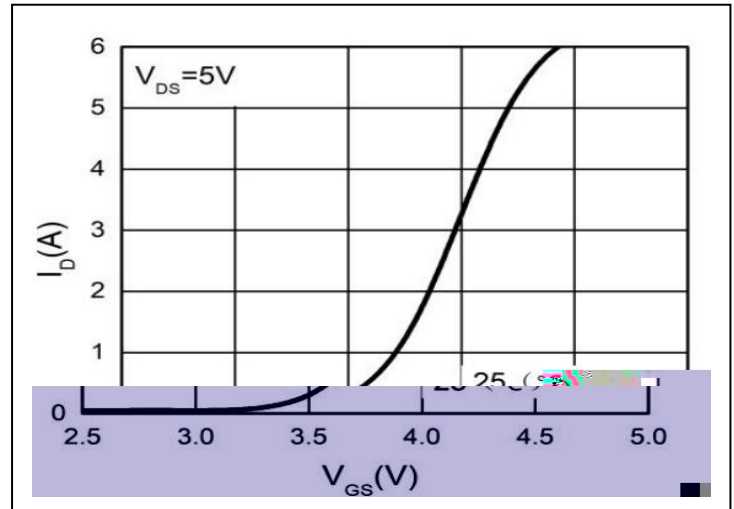
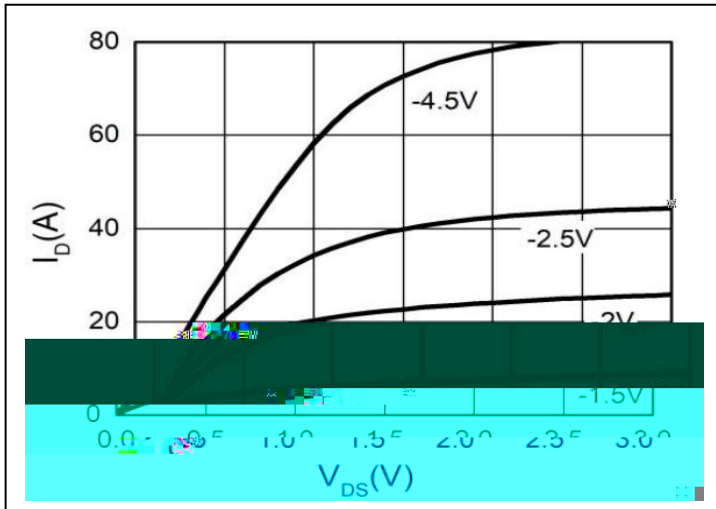


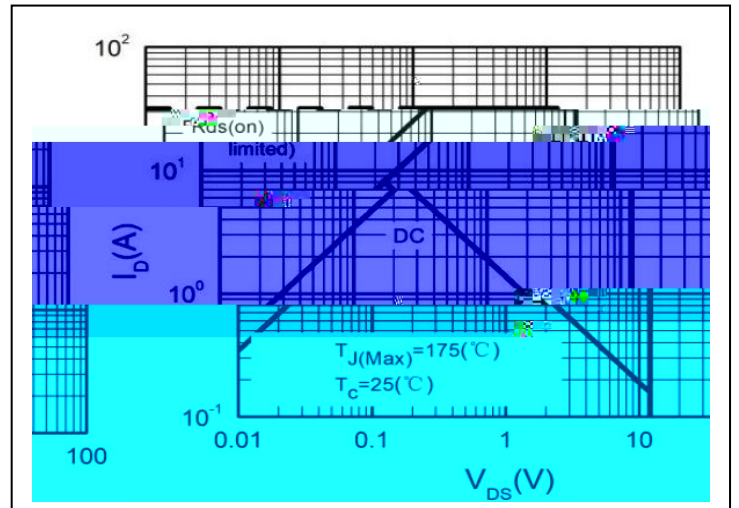
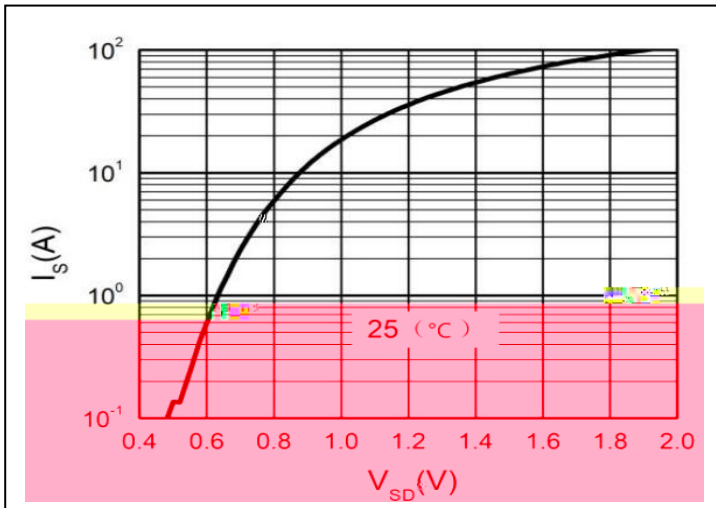
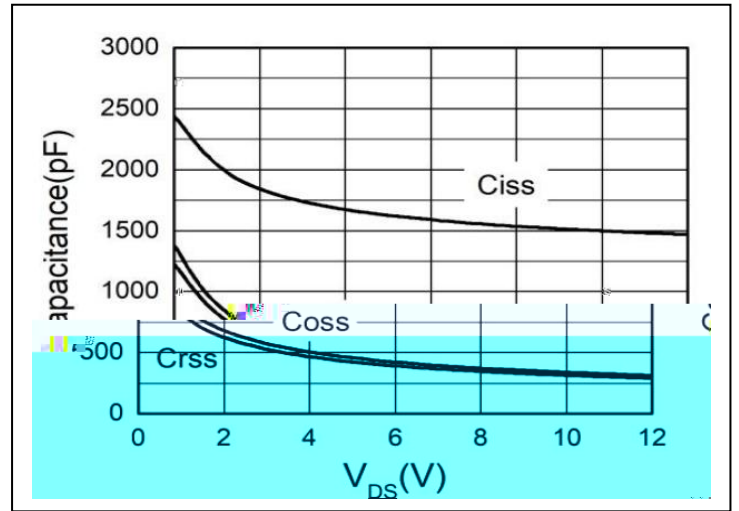
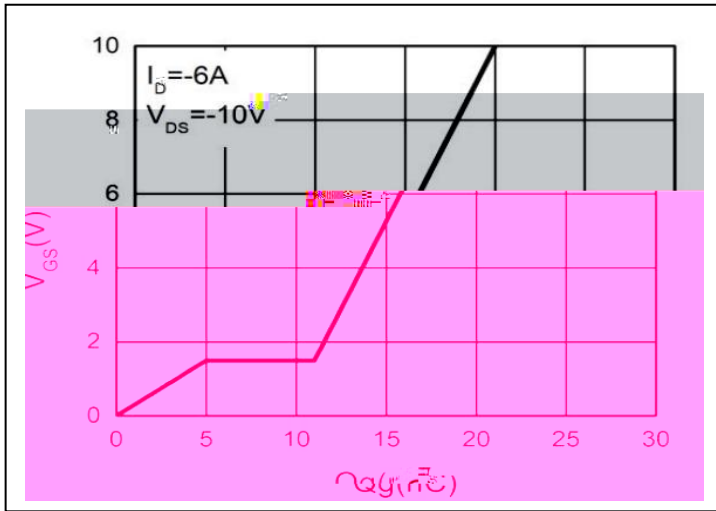
Calculated continuous current based on maximum allowable junction temperature.

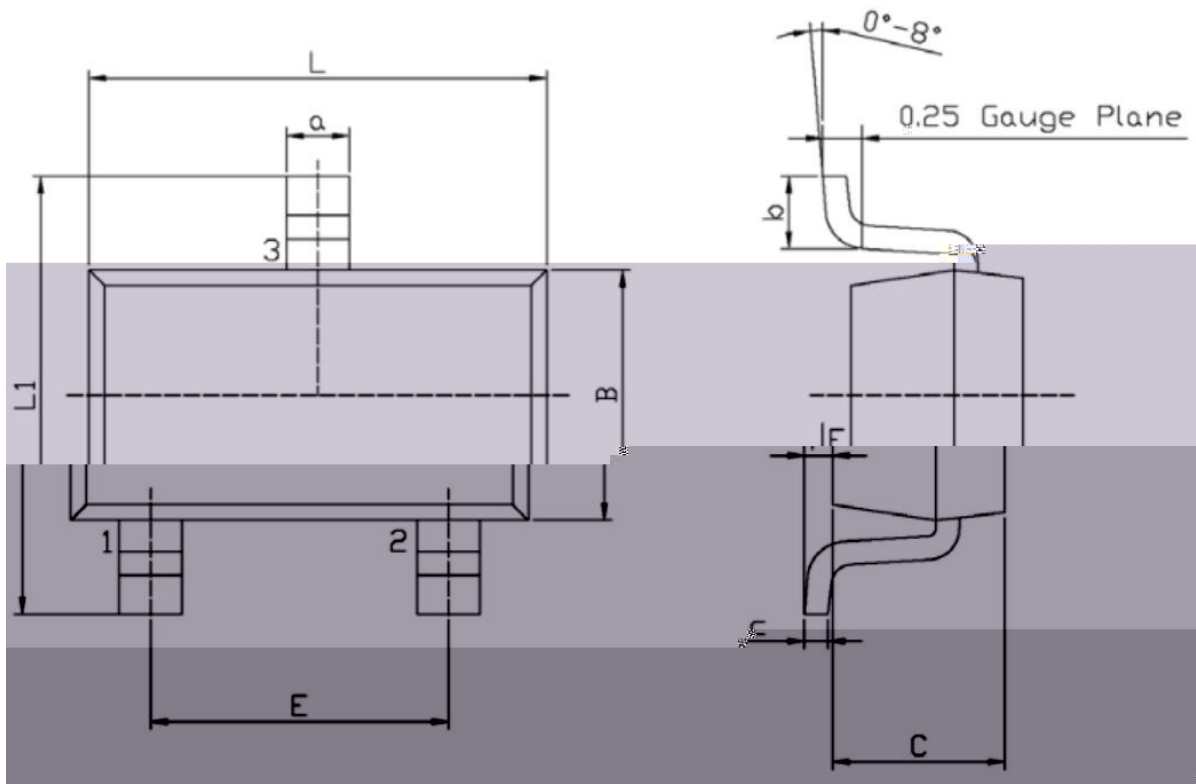
Repetitive rating; pulse width limited by max. junction temperature.

The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.

The value of  $R_{JA}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$







Unit: mm

Symbol	Dimensions In Millimeters		Symbol	Dimensions In Millimeters	
	Min	Max		Min	Max
L	2.82	3.02	a	0.35	0.50
B	1.50	1.70	c	0.15	0.20
C	0.90	1.30	b	0.35	0.55
L1	2.60	3.00	F	0	0.15
E	1.50	2.00			



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