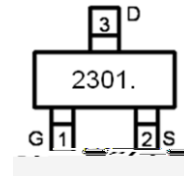


**Main Product Characteristics:**

$V_{DSS}$	-20V
$R_{DS(on)}$	59m (typ.)
$I_D$	-2.6A



SOT-23



Marking and Pin Assignments

Schematic Diagram

**Features and Benefits**

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150 operating temperature



**Description:**

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

**Absolute Max Rating:**

Symbol	Parameter	Max.	Units
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	-2.6	A
$I_{DM}$	Pulsed Drain Current	-10	
$P_D @ T_C = 25^\circ C$	Power Dissipation	1.25	W
$V_{DS}$	Drain-Source Voltage	-20	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 12$	V
$T_J \quad T_{STG}$	Operating Junction and Storage Temperature Range	-55 to +150	$^\circ C$

### Thermal Resistance

Symbol	Characterizes	Typ.	Max.	Units
R	Junction-to-case		100	/W

### Electrical Characterizes @ $T_A=25$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source breakdown voltage	-20			V	$V_{GS} = 0V, I_D = -$
$R_{DS(on)}$	Static Drain-to-Source on-resistance		59	75	m	$V_{GS}=-4.5V, I_D = -2A$
			76	95	m	$V_{GS}=-2.5V, I_D = -1.8A$
$V_{GS(th)}$	Gate threshold voltage	-0.4		-1	V	$V_{DS} = V_{GS}, I_D = -$
$I_{DSS}$	Drain-to-Source leakage current			-1		$V_{DS} = -20V, V_{GS} = 0V$
$I_{GSS}$	Gate-to-Source forward leakage			100	nA	$V_{GS} = 12V$
				-100		$V_{GS} = -12V$
$Q_g$	Total gate charge		5.8		nC	$I_D = -2.3A,$ $V_{DS}=-6V,$ $V_{GS} = -4.5V$
$Q_{gs}$	Gate-to-Source charge		0.84			
$Q_{gd}$	Gate-to-Drain("Miller") charge		1.6			
$t_{d(on)}$	Turn-on delay time		7		ns	$V_{GS}=-4.5V, V_{DD}=-20V,$ $R_{GEN}=3$ $R_L=10$
$t_r$	Rise time		14			
$t_{d(off)}$	Turn-Off delay time		20			
$t_f$	Fall time		7			
$C_{iss}$	Input capacitance					



**Test Circuits and Waveforms**

Typical Electrical and Thermal Characteristics



Figure1. Typical Output Characteristics

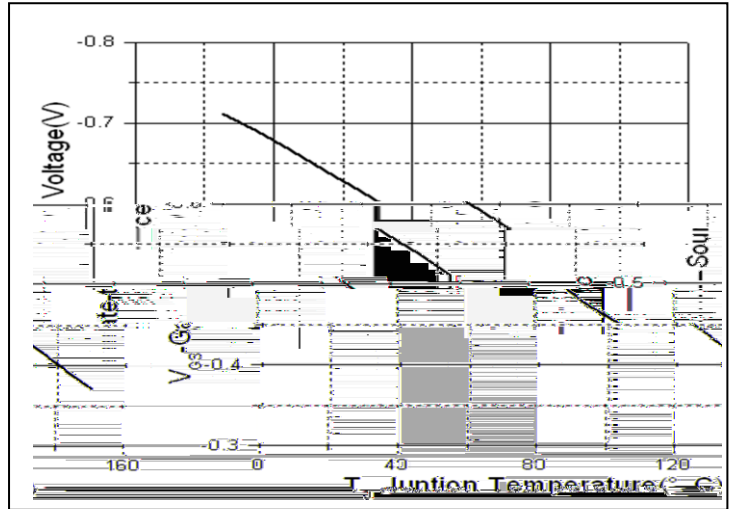


Figure2.  $V_{th}$  vs. Junction Temperature

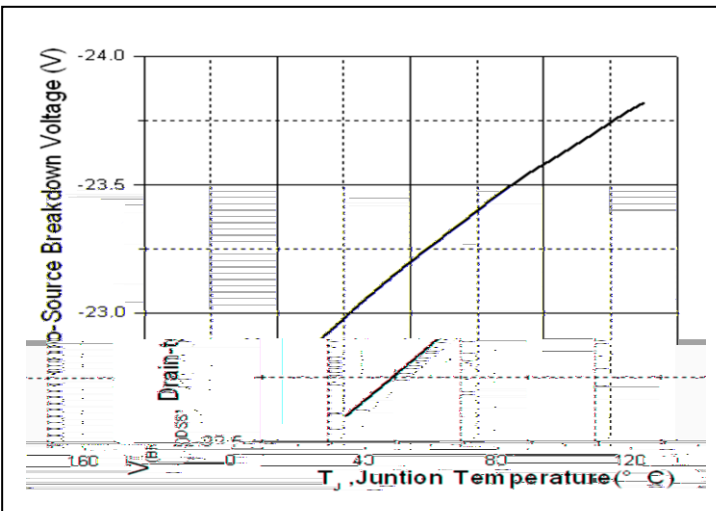


Figure3.  $V_{DS}$  vs. Junction Temperature

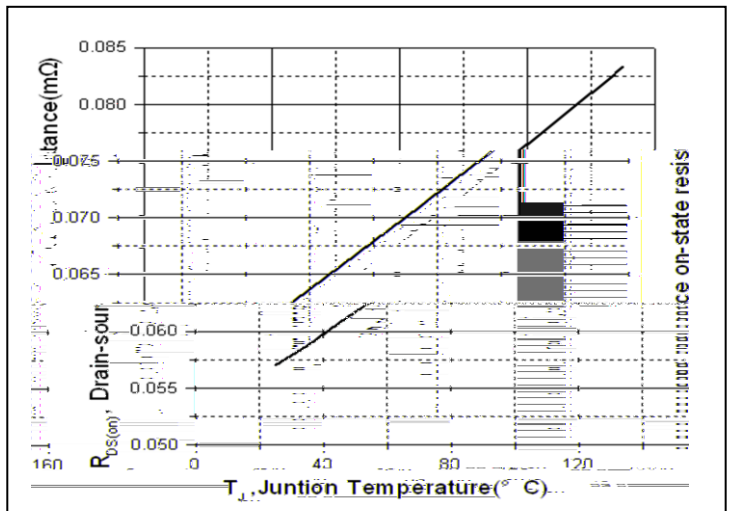


Figure4.  $R_{DS(on)}$  vs. Drain Current

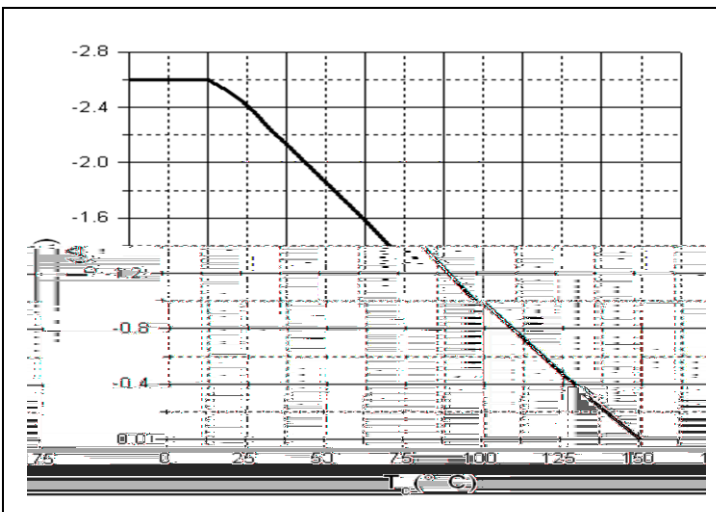


Figure5.  $V_{GS}$  vs. Junction Temperature

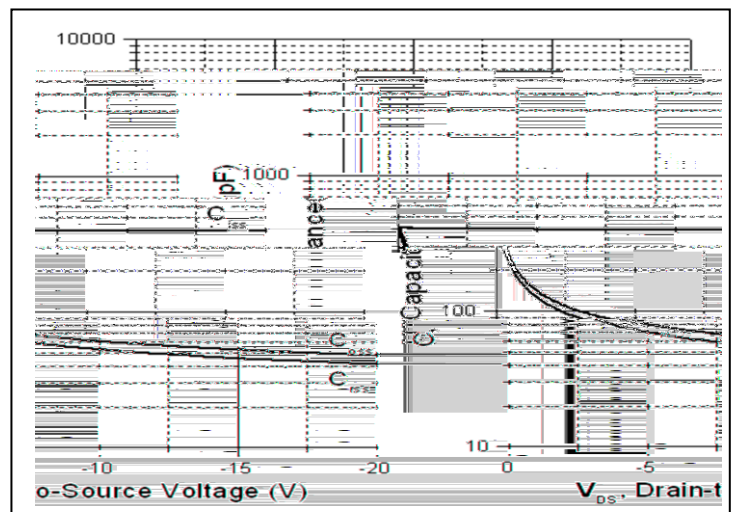
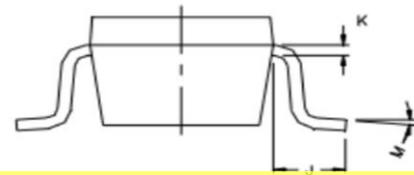
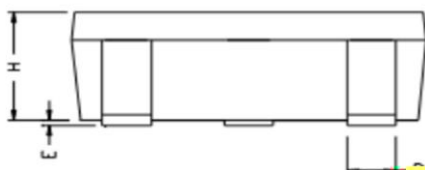
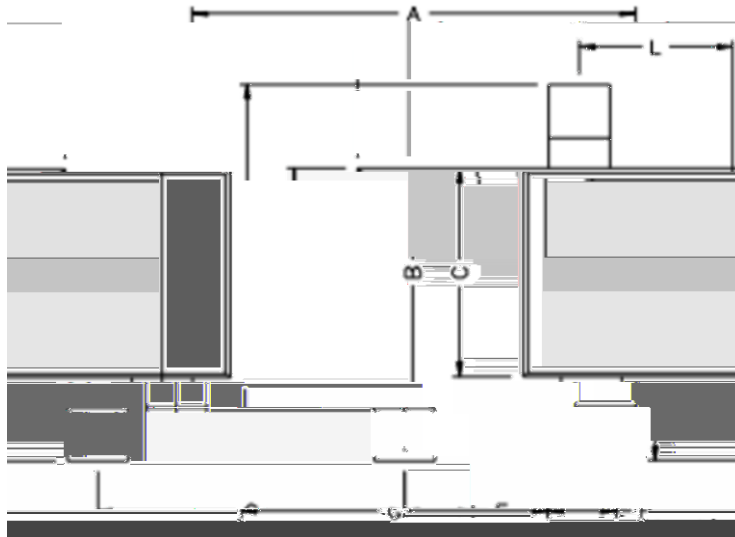


Figure6.  $C_{iss}$  vs.  $V_{GS}$



### Mechanical Data

SOT-23 Package Outline(Unit:mm)



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.80	3.00	G	1.80	2.00
B	2.30	2.50	H	0.90	1.1
C	1.20	1.40	K	0.10	0.20
D	0.30	0.50			
F	0.45	0.55	M	0°	10°

**ATTENTION:**

Any and all Silikron products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Silikron representative nearest you before using any Silikron products described or contained herein in such applications.

Silikron assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Silikron products described or contained herein.

Specifications of any and all Silikron products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the

equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer

Silikron Microelectronics (Suzhou) Co.,Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all Silikron products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of Silikron Microelectronics (Suzhou) Co.,Ltd.

Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. Silikron believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the Silikron product that you intend to use.